


IEEE Xplore®
RELIABLE INFORMATION

 Welcome
 United States Patent and Trademark Office

IEEE Xplore®
 1 Million Documents
 1 Million Users

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)
[Quick Links](#)
[» Search Results](#)
Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Table of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Xplore®

- ☐ Access the IEEE Enterprise File Cabinet

Print Format

Your search matched **13** of **1074479** documents.
 A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set

Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

1 Partial column FFT pipelines

Gorman, S.F.; Wills, J.M.;

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on] , Volume: 42 , Issue: 6 , June 1995

Pages:414 - 423

[\[Abstract\]](#) [\[PDF Full-Text \(740 KB\)\]](#) **IEEE JNL**

2 Group delay measurements of a transmission line using single channel measurements

van Wassenhove, D.; Schoukens, J.;

Instrumentation and Measurement, IEEE Transactions on , Volume: 39 , Issue: 1 , Feb 1990

Pages:180 - 183

[\[Abstract\]](#) [\[PDF Full-Text \(312 KB\)\]](#) **IEEE JNL**

3 A new memory reference reduction method for FFT implementation on DSP

Yiyan Tang; Lie Qian; Yuke Wang; Savaria, Y.;

Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on , Volume: 4 , 25-28 May 2003

Pages:IV-496 - IV-499 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(444 KB\)\]](#) **IEEE CNF**

4 Address generators for mapping arrays in bit-reversed order

Harley, T.R.; Maheshwaramurthy, G.P.;

Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEEE Transactions on] , Volume: 52 , Issue: 6 , June 2004

Pages:1693 - 1703

[\[Abstract\]](#) [\[PDF Full-Text \(552 KB\)\]](#) **IEEE JNL**

5 Fast Fourier transforms over finite groups by multiprocessor systems

Roziner, T.D.; Karpovsky, M.G.; Trachtenberg, L.A.;

Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 38 , Issue: 2 , Feb. 1990
Pages:226 - 240

[\[Abstract\]](#) [\[PDF Full-Text \(1168 KB\)\]](#) IEEE JNL

6 Automatic face recognition using neural networks

El-Bakry, H.M.; Abo-Elsoud, M.A.; Kamel, M.S.;

Microelectronics, 1999. ICM '99. The Eleventh International Conference on , 22-24 Nov. 1999
Pages:105 - 108

[\[Abstract\]](#) [\[PDF Full-Text \(320 KB\)\]](#) IEEE CNF

7 Real time modal analysis of power system oscillations

Hiyama, T.; Suzuki, N.;

Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE International Symposium on , Volume: 4 , 28-31 May 2000
Pages:225 - 228 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) IEEE CNF

8 Modular neural networks for face detection

El-Bakry, H.M.; Abo-Elsoud, M.A.; Kanel, M.S.;

Radio Science Conference, 2000. 17th NRSC '2000. Seventeenth National , 22-24 Feb. 2000
Pages:C3/1 - C3/8

[\[Abstract\]](#) [\[PDF Full-Text \(680 KB\)\]](#) IEEE CNF

9 Efficient computation of the discrete Wigner distribution function through a new iterative algorithm

Garcia, I.; Gonzalo, C.; Castellanos, M.P.; Moreno, J.A.; Sanchez-Dehesa, J.M.;

Acoustics, Speech, and Signal Processing, 1997. ICASSP-97., 1997 IEEE International Conference on , Volume: 3 , 21-24 April 1997
Pages:1981 - 1984 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(300 KB\)\]](#) IEEE CNF

10 Granularity experiments for the adaptive multiprocessor system (AMPS)

Carlson, B.S.; Lo, N.W.; Tao, D.L.; Zhu Zheng;

Circuits and Systems, 1996., IEEE 39th Midwest symposium on , Volume: 3 , 18-21 Aug. 1996
Pages:1285 - 1288 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(360 KB\)\]](#) IEEE CNF

11 An effective memory addressing scheme for multiprocessor FFT system

Dawoud, D.S.;

Africon Conference in Africa, 2002. IEEE AFRICON. 6th , Volume: 1 , 2-4 Oct. 2002

Pages:29 - 34 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(385 KB\)\]](#) IEEE CNF

12 Fast cooperative modular neural nets for human face detection*El-Bakry, H.M.;*

Image Processing, 2001. Proceedings. 2001 International Conference on , Volume: 1 , 7-10 Oct. 2001

Pages:1002 - 1005 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(424 KB\)\]](#) [IEEE CNF](#)

13 Programmable architecture for matrix and signal processing*Hamilton, B.;*

IEEE Region 5 Conference, 1988: 'Spanning the Peaks of Electrotechnology' , 21-23 March 1988

Pages:116 - 120

[\[Abstract\]](#) [\[PDF Full-Text \(384 KB\)\]](#) [IEEE CNF](#)

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC](#) | [Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved

L Number	Hits	Search Text	DB	Time stamp
1	31718	710/\$.ccls. or 712/\$.ccls. or 708/\$.ccls. and (FFT or DCT or Fourier or (cosine adj transform))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 12:44
2	28338	(710/\$.ccls. or 712/\$.ccls. or 708/\$.ccls. and (FFT or DCT or Fourier or (cosine adj transform))) and (buffer\$1 or memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 13:11
3	12955	((710/\$.ccls. or 712/\$.ccls. or 708/\$.ccls. and (FFT or DCT or Fourier or (cosine adj transform))) and (buffer\$1 or memory)) and group\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 13:11
4	5143	((((710/\$.ccls. or 712/\$.ccls. or 708/\$.ccls. and (FFT or DCT or Fourier or (cosine adj transform))) and (buffer\$1 or memory)) and group\$4) and internal and external	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 12:47
5	474	(((((710/\$.ccls. or 712/\$.ccls. or 708/\$.ccls. and (FFT or DCT or Fourier or (cosine adj transform))) and (buffer\$1 or memory)) and group\$4) and internal and external) and (FFT or (fourier adj transform)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 12:47
6	464	(((((710/\$.ccls. or 712/\$.ccls. or 708/\$.ccls. and (FFT or DCT or Fourier or (cosine adj transform))) and (buffer\$1 or memory)) and group\$4) and internal and external) and (FFT or (fourier adj transform))) and block\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 12:48
7	157	(((((710/\$.ccls. or 712/\$.ccls. or 708/\$.ccls. and (FFT or DCT or Fourier or (cosine adj transform))) and (buffer\$1 or memory)) and group\$4) and internal and external) and (FFT or (fourier adj transform))) and block\$1) and 708/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 12:48
8	307	(((((710/\$.ccls. or 712/\$.ccls. or 708/\$.ccls. and (FFT or DCT or Fourier or (cosine adj transform))) and (buffer\$1 or memory)) and group\$4) and internal and external) and (FFT or (fourier adj transform))) and block\$1) not	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 13:06
9	194	(((((710/\$.ccls. or 712/\$.ccls. or 708/\$.ccls. and (FFT or DCT or Fourier or (cosine adj transform))) and (buffer\$1 or memory)) and group\$4) and internal and external) and (FFT or (fourier adj transform))) and block\$1) and 708/\$.ccls.) (DMA or (direct adj memory adj access)) and (((((710/\$.ccls. or 712/\$.ccls. or 708/\$.ccls. and (FFT or DCT or Fourier or (cosine adj transform))) and (buffer\$1 or memory)) and group\$4) and internal and external) and (FFT or (fourier adj transform))) and block\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 13:12
10	34	((DMA or (direct adj memory adj access)) and (((((710/\$.ccls. or 712/\$.ccls. or 708/\$.ccls. and (FFT or DCT or Fourier or (cosine adj transform))) and (buffer\$1 or memory)) and group\$4) and internal and external) and (FFT or (fourier adj transform))) and block\$1)) and 708/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 13:11
11	320	708/404.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/22 13:11

12	234	708/404.ccls. and (buffer\$1 or memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/22 13:11
13	99	(708/404.ccls. and (buffer\$1 or memory)) and group\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/22 13:12
14	6	(DMA or (direct adj memory adj access)) and ((708/404.ccls. and (buffer\$1 or memory)) and group\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/22 13:12
15	96	((708/404.ccls. and (buffer\$1 or memory)) and group\$4) and (input or output)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/22 13:13
-	2	6600496.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/22 12:43